Docket No. SPI

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig, et al.

Serial No.:

10/066,456

Filing Date:

1/31/02

For:

METHOD AND APPARATUS FOR

PERFORMING TECHNOLOGY

**MAPPING** 

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit for consideration in the aboveidentified application the documents listed on the attached Form PTO-1449. Copies of the documents are also submitted herewith. The Examiner is requested to make these documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

Submitted in this IDS are copies of a number of applications that have the specifications

and drawings similar to the present application. The serial number of each application is written

on the first page of each the application specification.

This Information Disclosure Statement is submitted within three months of the

application filing date or before the mailing date of a first Office Action on the merits;

accordingly, no fee or separate requirements are required.

This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a

representation that: (i) a complete search has been made; (ii) additional information material to

the examination of this application does not exist; (iii) the information, protocols, results and the

like reported by third parties are accurate or enabling; or (iv) the above information constitutes

prior art to the subject invention.

In the unlikely event that the transmittal letter is separated from this document and the

Patent Office determines that an extension and/or other relief is required, applicant petitions for

any required relief including extensions of time and authorizes the Assistant Commissioner to

charge the cost of such petitions and/or other fees due in connection with the filing of this

document to Deposit Account No. 50-1128 referencing SPLX.P0099. However, the Assistant

Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated:

March 31, 2003

Respectfi

By:

Registration No. 39,585

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Serial No. 10/066,456 Docket No. SPLX.P0099

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PTO/SB/21 (6-98) (modified)
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## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	10/066,456				
Filing Date	1/31/02				
First Named Inventor	Steven Teig, et al.				
Group Art Unit	2825				
Examiner Name	<none yet=""></none>				
Attorney Docket Number	SPLX.P0099				

Enclosures (Check all that apply)							
Fee Transmittal Form		Assignment Papers (for an application)				After Al	lowance Communication to
Fee Attached			Draw	ving(s)			Communication to Board of s and Interferences
Amendment/Reply			Licer	nsing-related Papers		Appeal	Communication to Group
After Final			Petiti	ion		Proprie	tary Information
Affidavits/declara	tions(s)			ion to Convert a Provisional cation		Status	Letter
Preliminary Amendm	ent						
Extension of Time Rec	quest			er of Attorney, Revocation age of Correspondence Address	×	Other E	Enclosures ( <i>Listed below</i> ):
Request to Rescind Properties Non-Publication Request		Terminal Disclaimer				23 References	
Information Disclosure Statement and PTO-1449			Requ	uest for Refund			
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT							
Firm Or Individual name Mani Adeli, Reg. No. 39, 585 of Stattler Johansen & Adeli LLP					eli LLP		
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Date March 31, 2003							
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**Application Number** 10/066,456 Substitute for form 1449A/PTO **Filing Date** 1/31/02 IBFORMATION DISCLOSURE First Named Invent r TATEMENT BY APPLICANT Steven Teig, et al. Group Art Unit 2825 (use as many sheets as necessary) **Examiner Name** Sheet 1 of 2 Attorney Docket Number SPLX.P0099

RELATED U.S. PATENT APPLICATIONS						
Examiner* Cite Application Initials No.1 Serial Number		Application Serial	Name of Patentee or Applicant of Cited Document	Date of Filing MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages o Relevant Figures Appear	
	1.	10/062,017	Steven Teig & Asmus Hetzel	01-31-02		
	2.	10/061,459	Steven Teig & Asmuz Hetzel	01-31-02		
	3.	10/062,014	Steven Teig & Asmus Hetzel	01-31-02		
	4.	10/062,044	Steven Teig & Asmuz Hetzel	01-31-02		
	5.	10/066,264	Steven Teig & Asmus Hetzel	01-31-02		
1	6.	10/066,188	Steven Teig & Asmus Hetzel	01-31-02		
	7.	10/062,992	Steven Teig & Asmuz Hetzel	01-31-02		
	8.	10/061,474	Steven Teig & Asmus Hetzel	01-31-02		
	9.	10/061,719	Steven Teig & Asmus Hetzel	01-31-02		
	10.	10/062,993	Steven Teig & Asmus Hetzel	01-31-02		
	11.	10/062,047	Steven Teig & Asmuz Hetzel	01-31-02		

				U.S. PATENT DOCUMENT	s	
Examiner* Initials	Cite No.1	Number Kir	cument nd Code <sup>2</sup> if known)	Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	12.	5,649,165		Jain et al.	07-15-97	толочин пушез дрреаг
	13.	6,301,687	B1	Jain et al.	10-09-01	

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	Date
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<sup>\*</sup> EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>&</sup>lt;sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if

Please type a plus sign (+) inside this box Applicati n Number 10/066,456 Substitute for form 1449A/PTO Filing Date 1/31/02 **INFORMATION DISCLOSURE** STATEMENT BY APPLICANT First Named Inv ntor Steven Teig, et al. **Group Art Unit** 2825 (use as many sheets as necessary) **Examiner Name** 2 Sheet 2 of Attorney Docket Number SPLX.P0099

		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Examiner* Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
	14.	D. Jongeneel, R. Otten, Y. Watanabe and R. K. Brayton, Area and Search Space Control for Technology Mapping, 37 <sup>th</sup> Design Automation Conference, 86-91, 2000.	
	15.	Henrik Reif Andersen, An Introduction to Binary Decision Diagrams, October 1997 (minor revisions April 1998), 36 pp.	
	16.	Jerry Burch and David Long, Efficient Boolean Function Matching, Proc. ICCAD 1992, 408-411.	
•	17.	John Fishburn, A Depth-Decreasing Heuristic for Combinational Logic; or How to Convert Ripple-Carry Adder into a Carry-Lookahead Adder or Anything In-Between, Proceedings of the 27 <sup>th</sup> Design Automation Conference, 361-364, 1990.	
>	18.	Kamal Chaudhary and Massoud Pedram, A Near Optimal Algorithm for Technology Mapping Minimizing Area under Delay Constraints, Proceedings of the 29 <sup>th</sup> Design Automation Conference, 492-498, 1992.	
	19.	Kurt Keutzer, DAGON: Technology Binding and Local Optimization by DAG Matching, Proceedings of the 24 <sup>th</sup> Design Automation Conference, 341-347, 1987	
	20.	Randal Bryant, Symbolic Boolean Manipulation with Ordered Binary Decision Diagrams, CMU CS Tech Report CMU-CS-92-160.	
	21.	Uwe Hinsberger and Reiner Kolla, Boolean Matching for Large Libraries, Proceedings of the 35 <sup>th</sup> Design Automation Conference, 206-211, June 1998.	
	22.	Yuji Kukimoto, Robert K. Brayton, and Prashant Sawkar. Delay-Optimal Technology Mapping by DAG Covering, Dept. of Electrical Engineering and Computer Science, University of California, Berkeley, Strategic CAD Laboratories, Intel Corp., October 1997.	
	23.	Zbigniew J. Czech, et al., An Optimal Algorithm for Generating Minimal Perfect Hashing Functions, Information Processing Letters, 43(5); 257-264, October 1992	

Examiner	Date	
Signature	Considered	

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